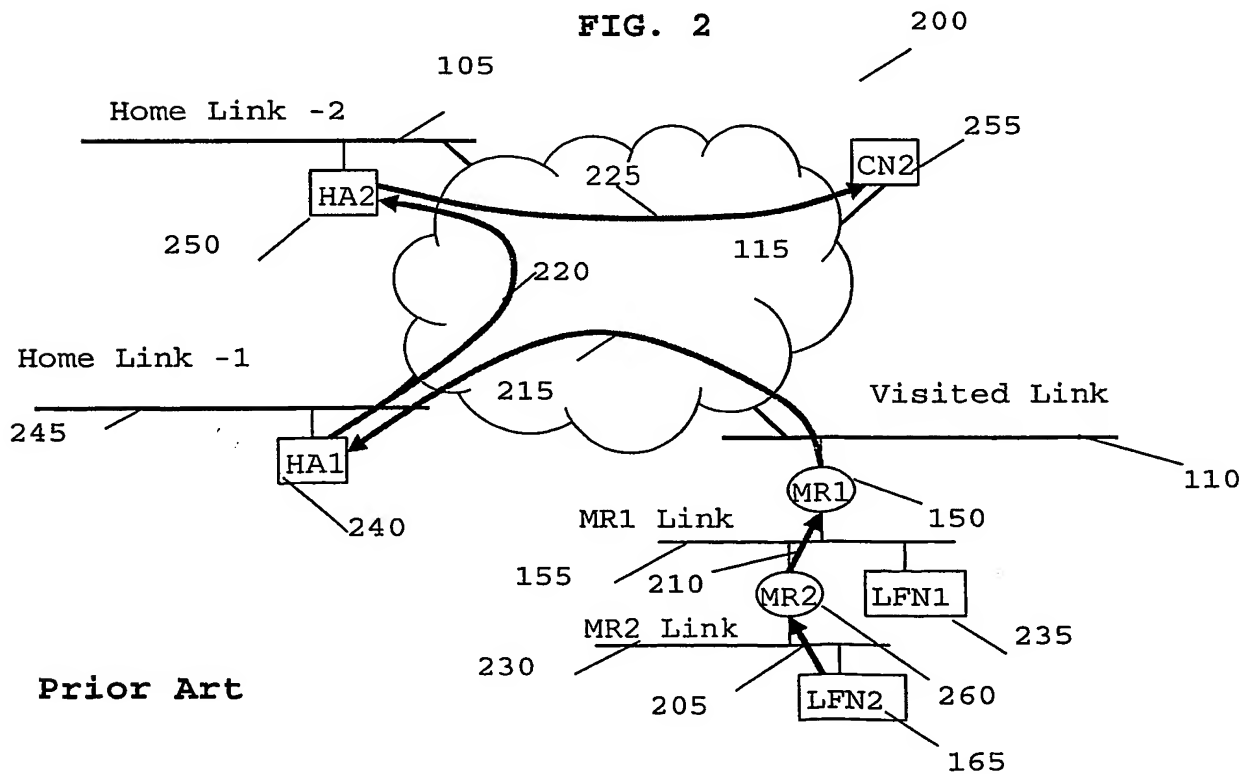
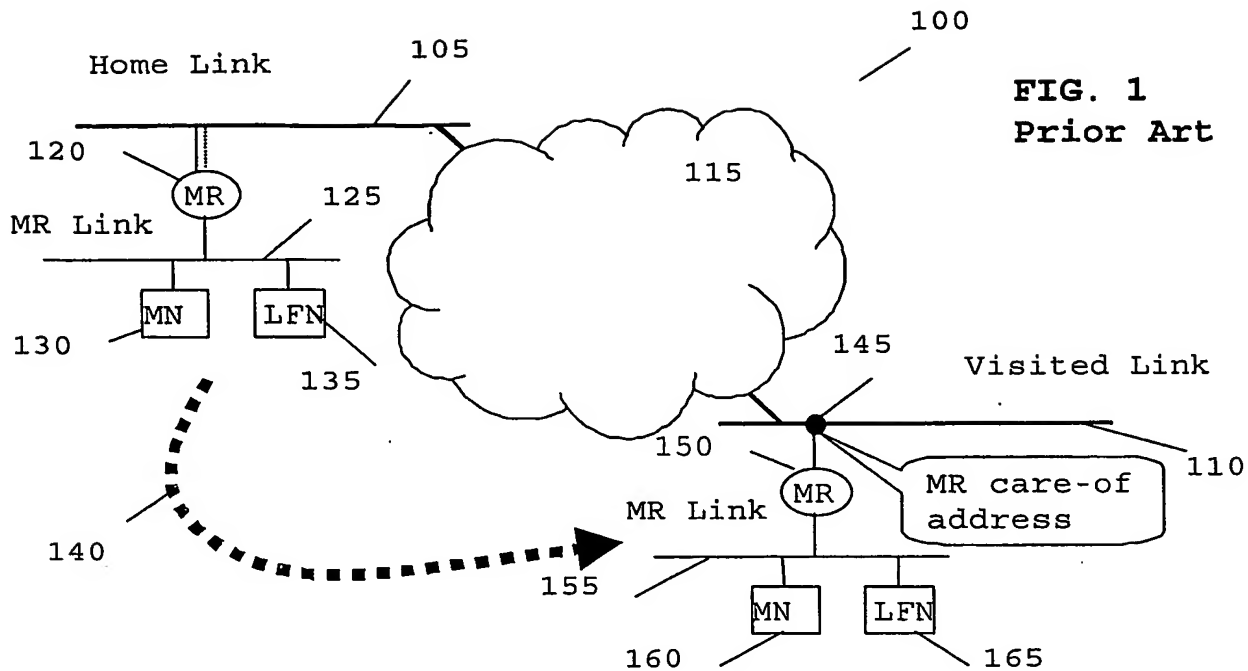


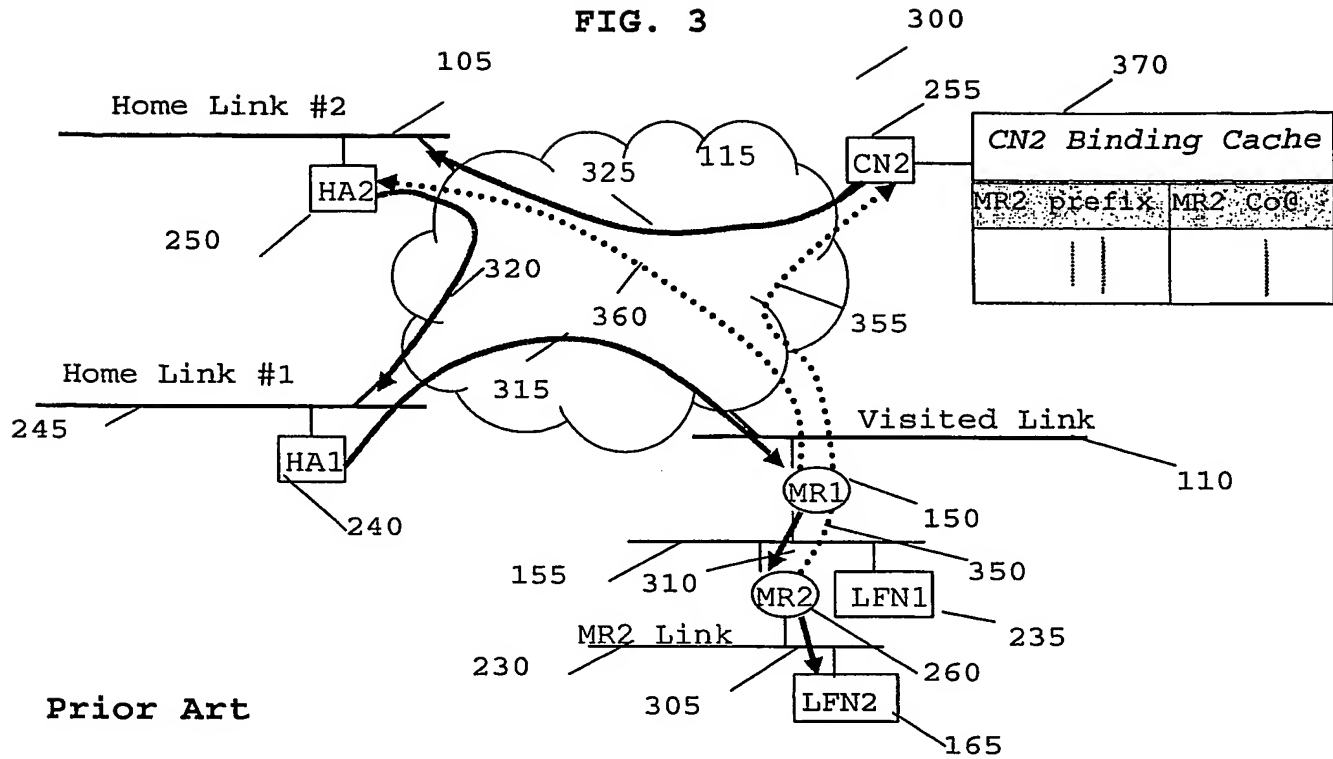
1/19



Prior Art

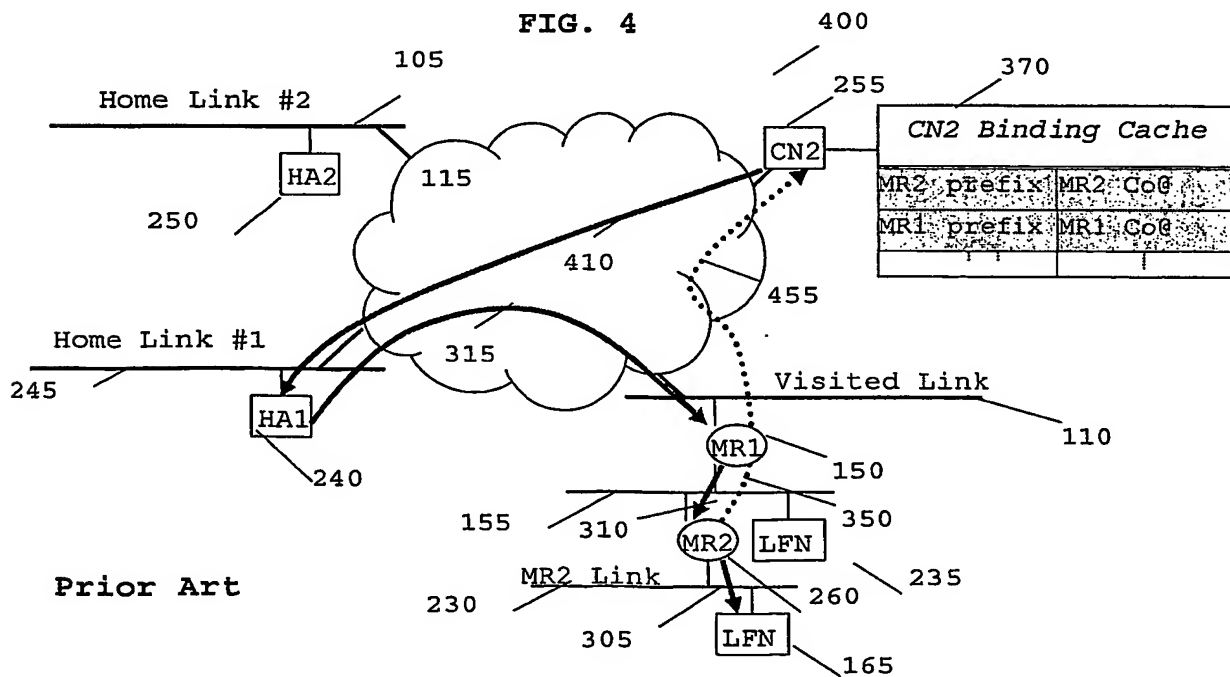
2/19

FIG. 3

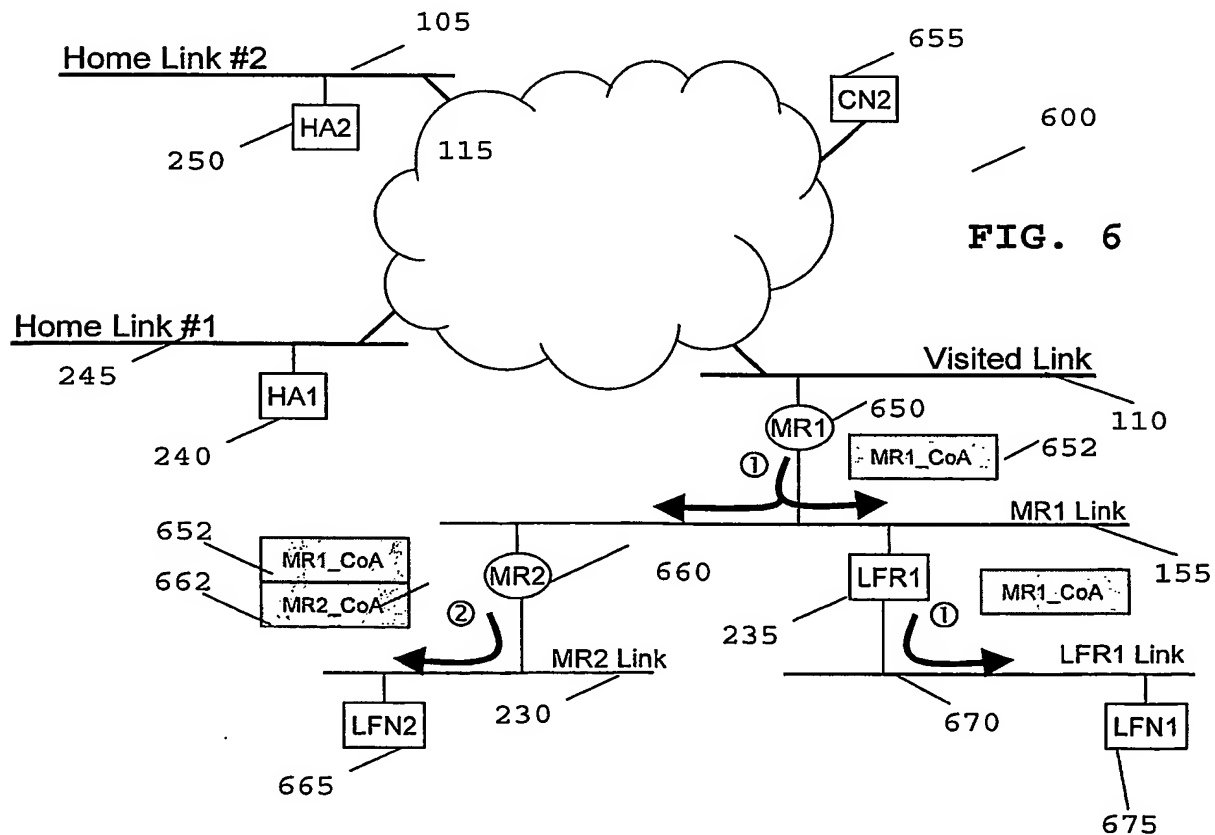
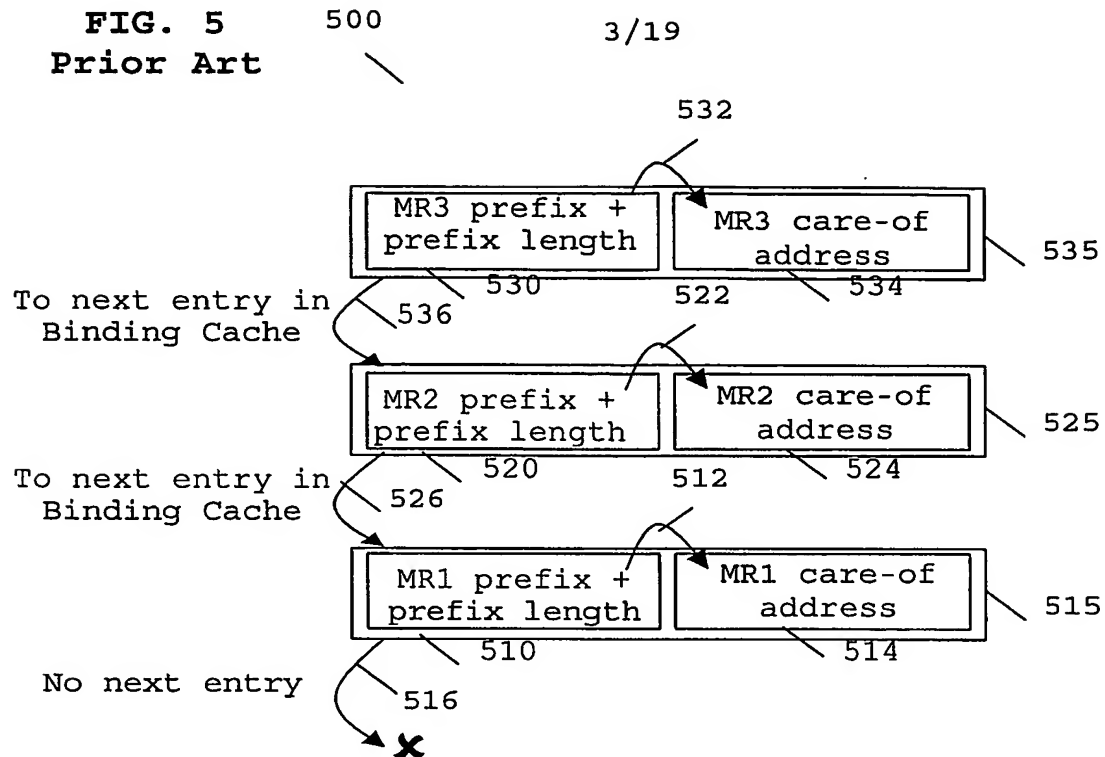


Prior Art

FIG. 4



Prior Art

FIG. 5
Prior Art

4/19

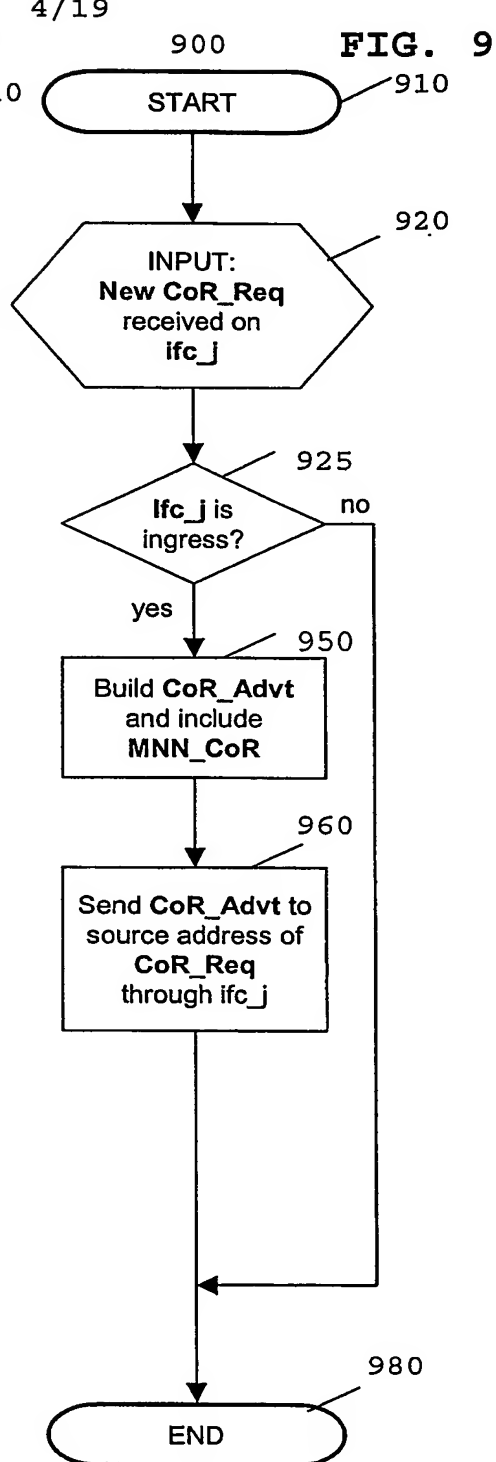
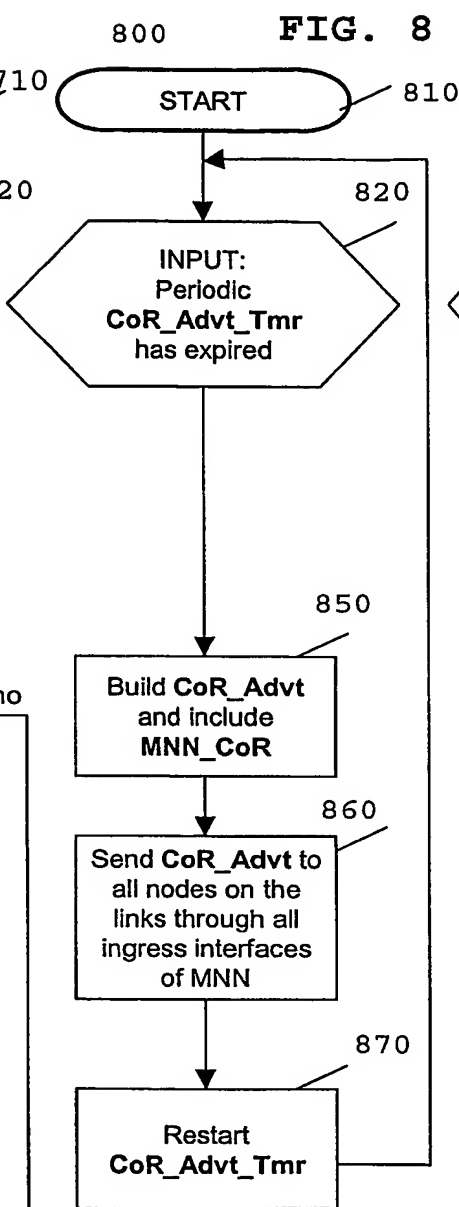
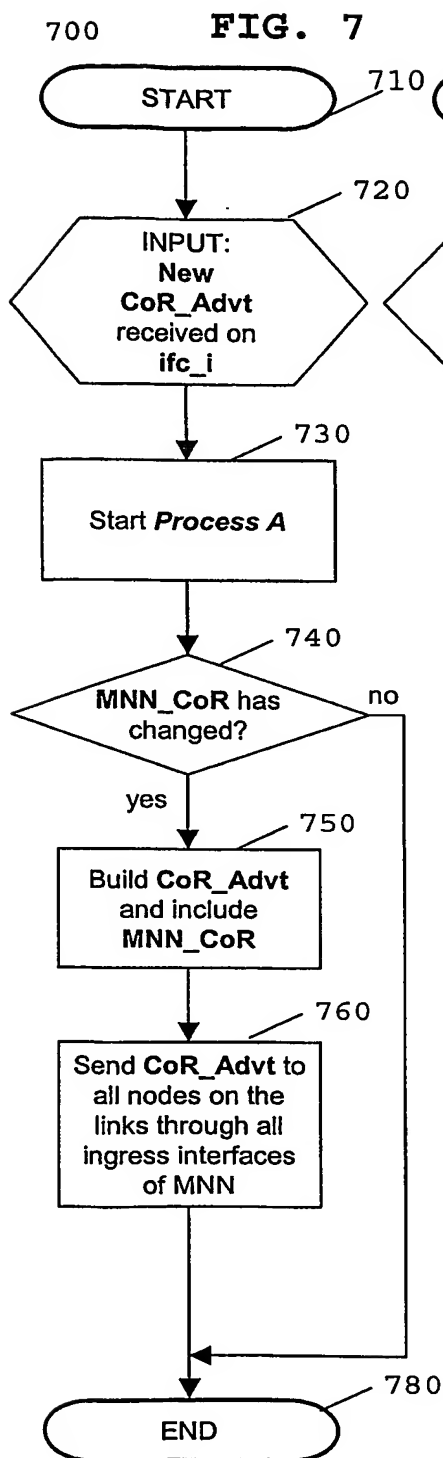
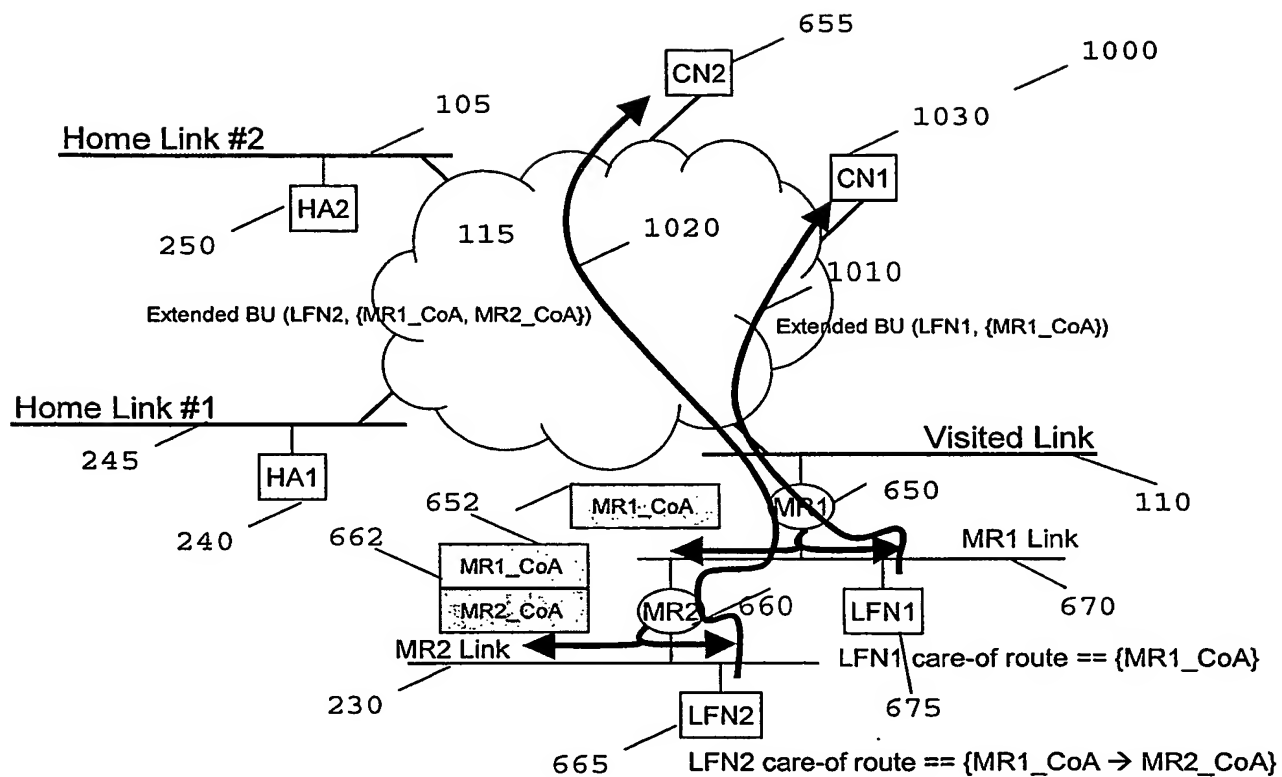
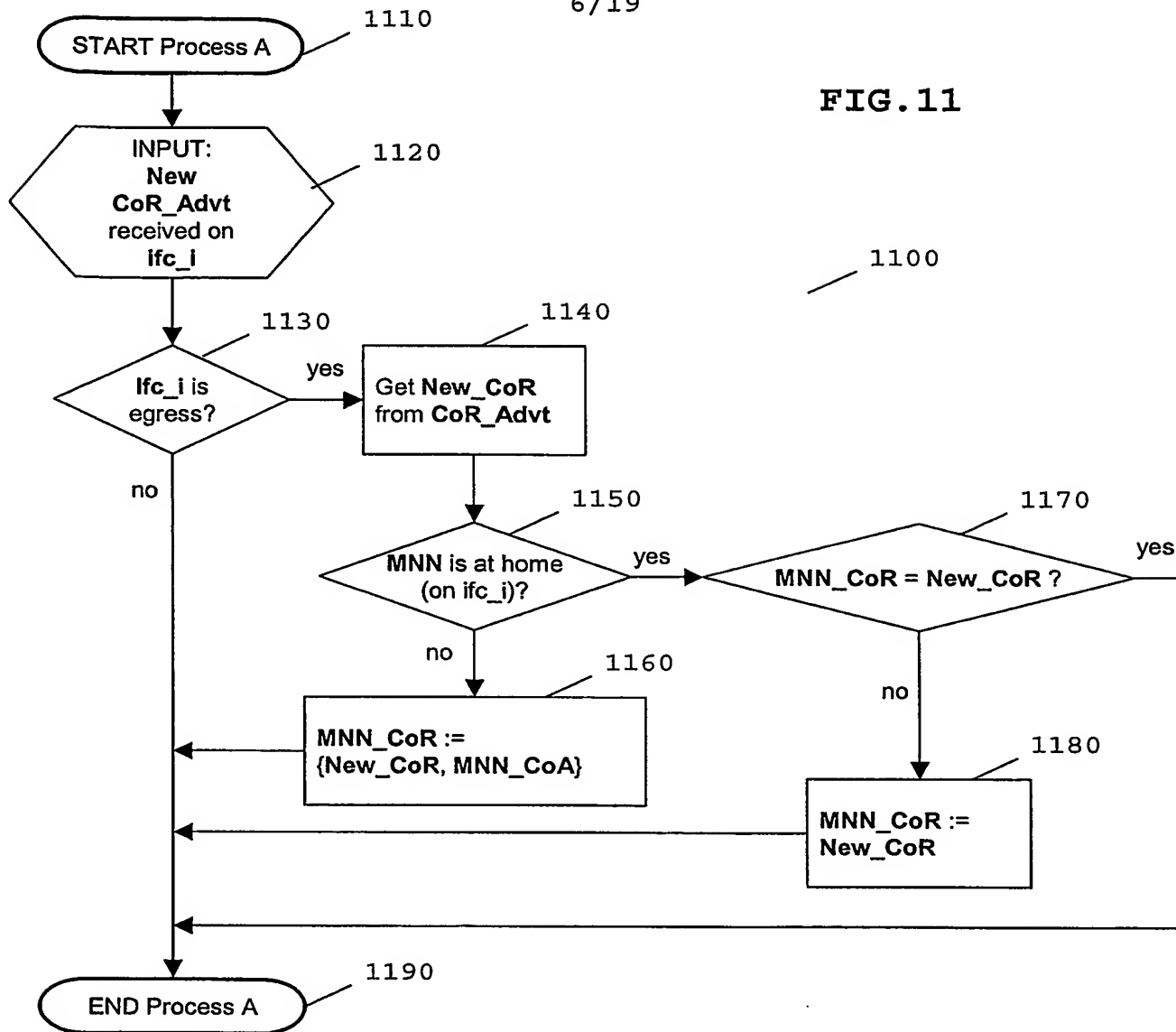


FIG. 10



6/19

FIG. 11



7/19

FIG. 12

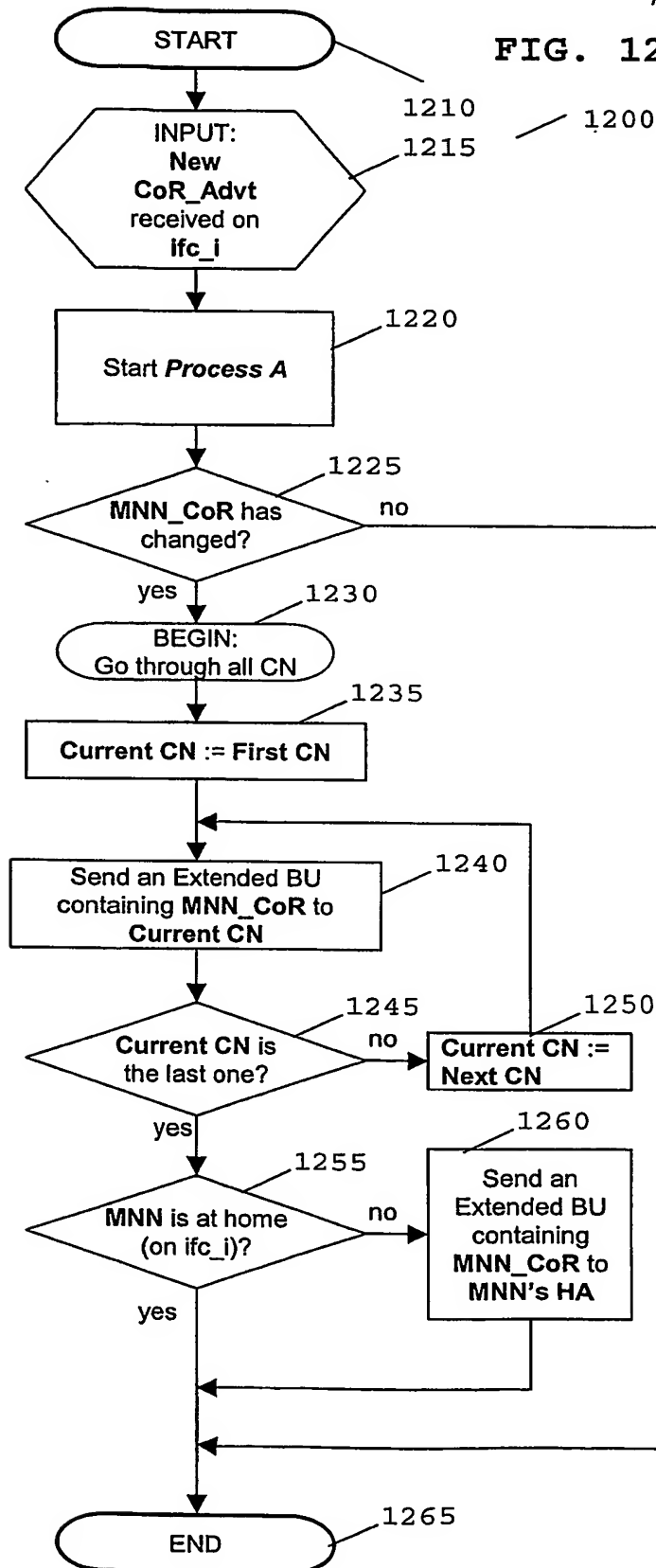
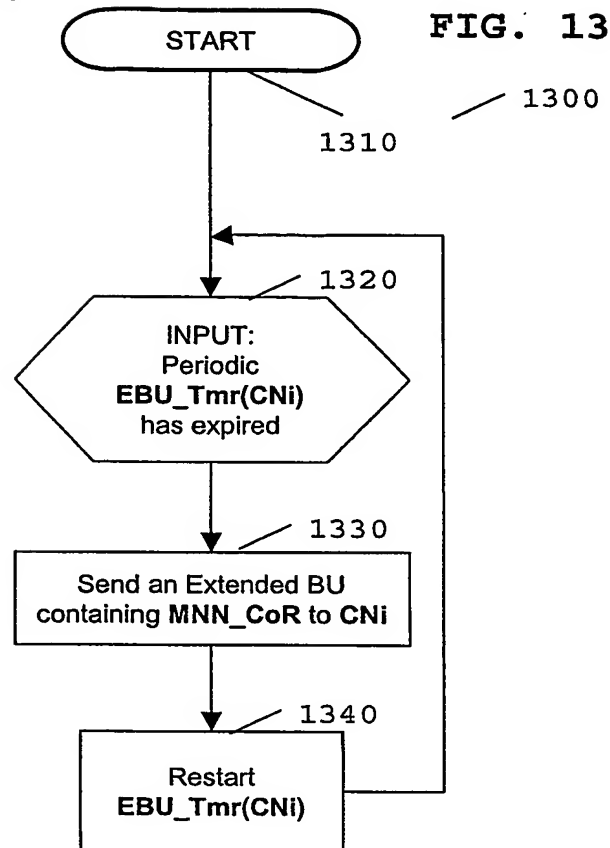
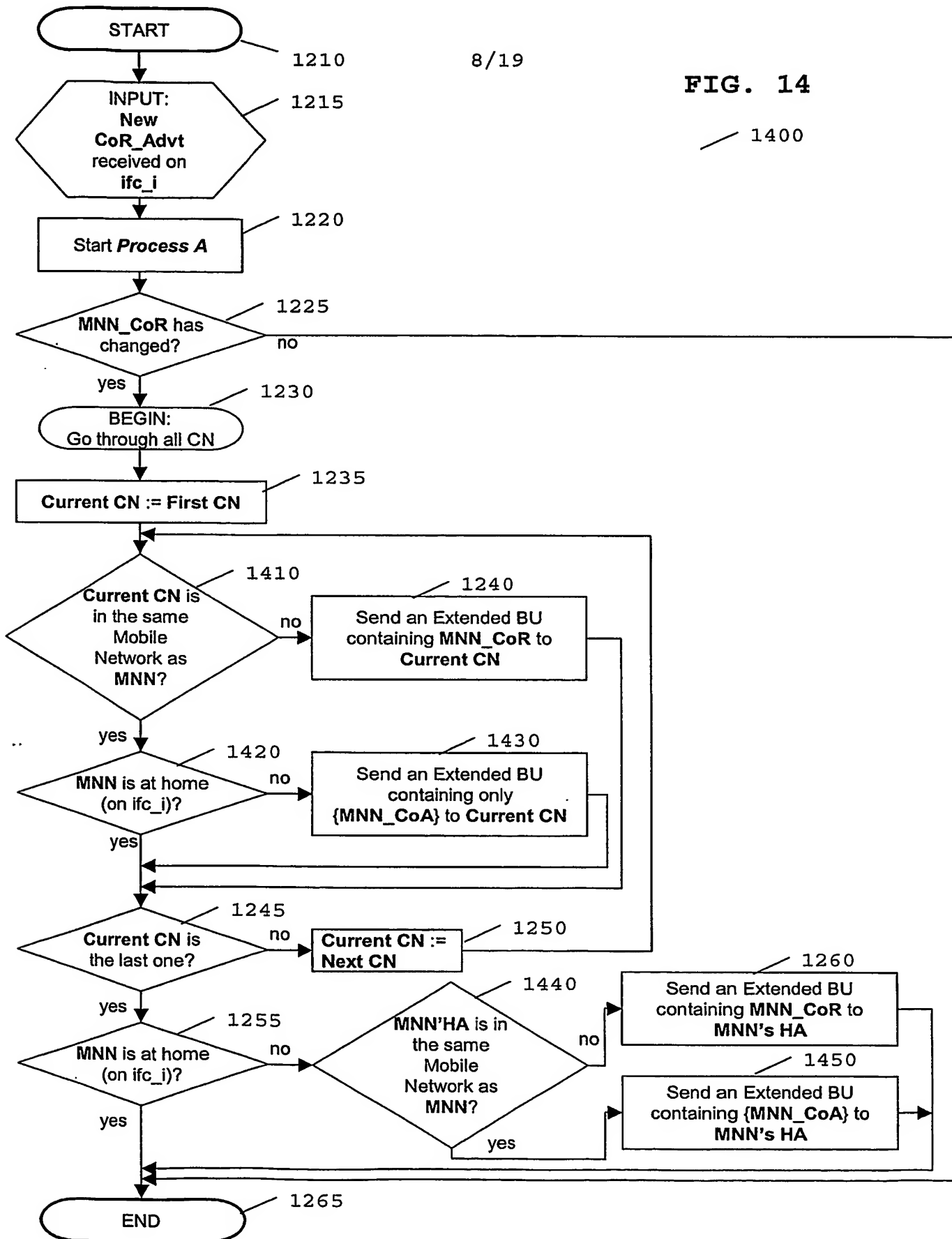


FIG. 13

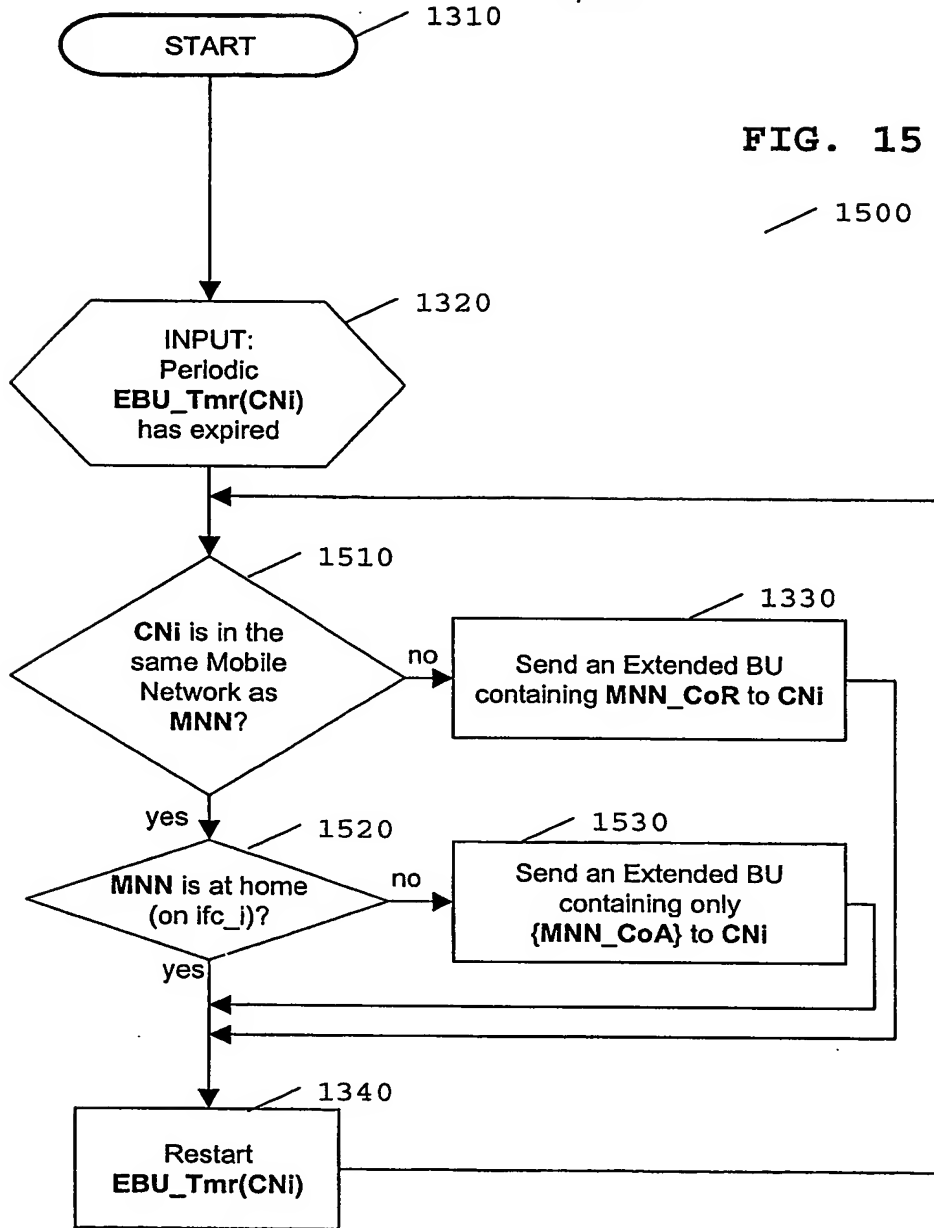


8/19

FIG. 14

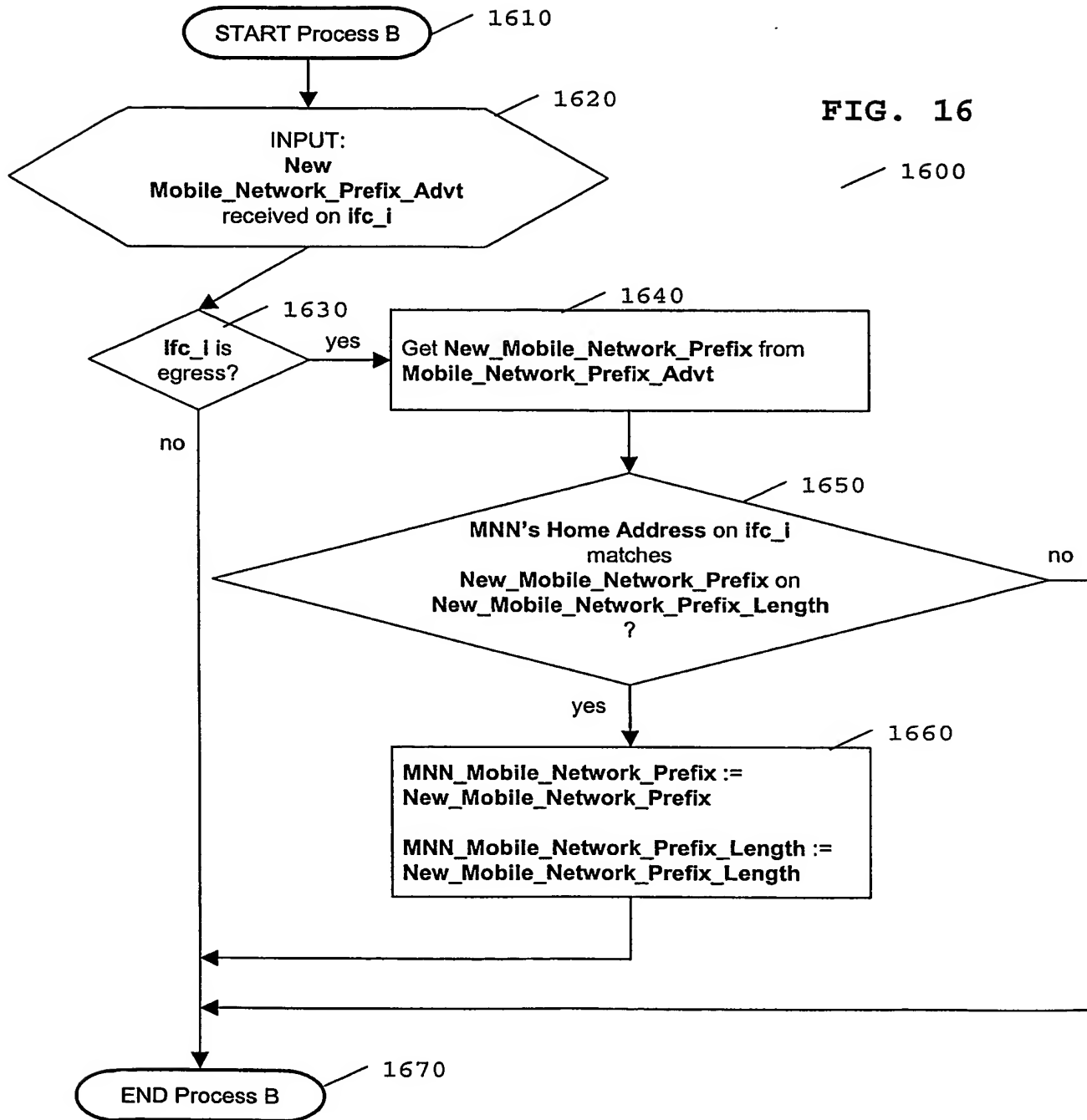


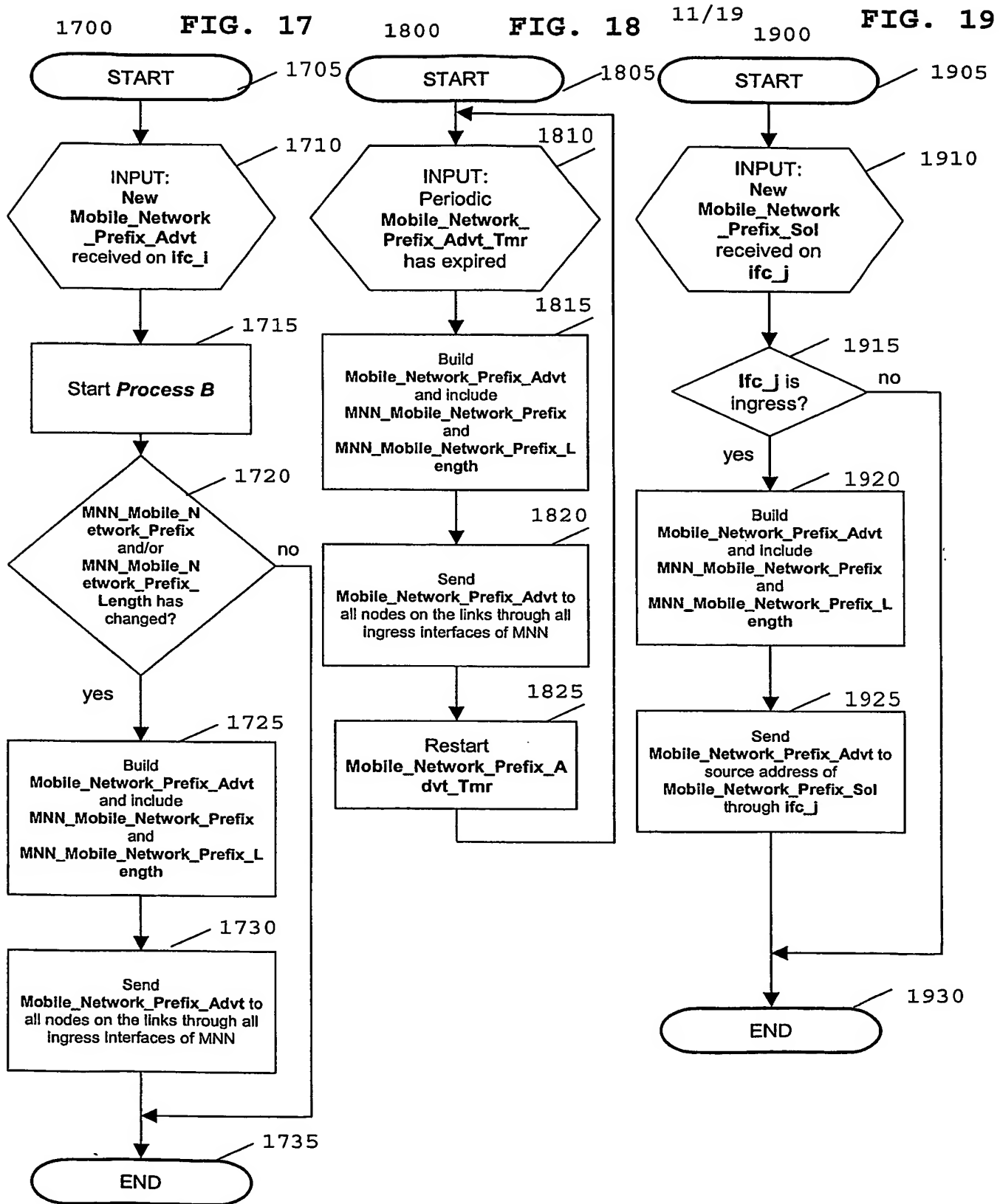
9/19



10/19

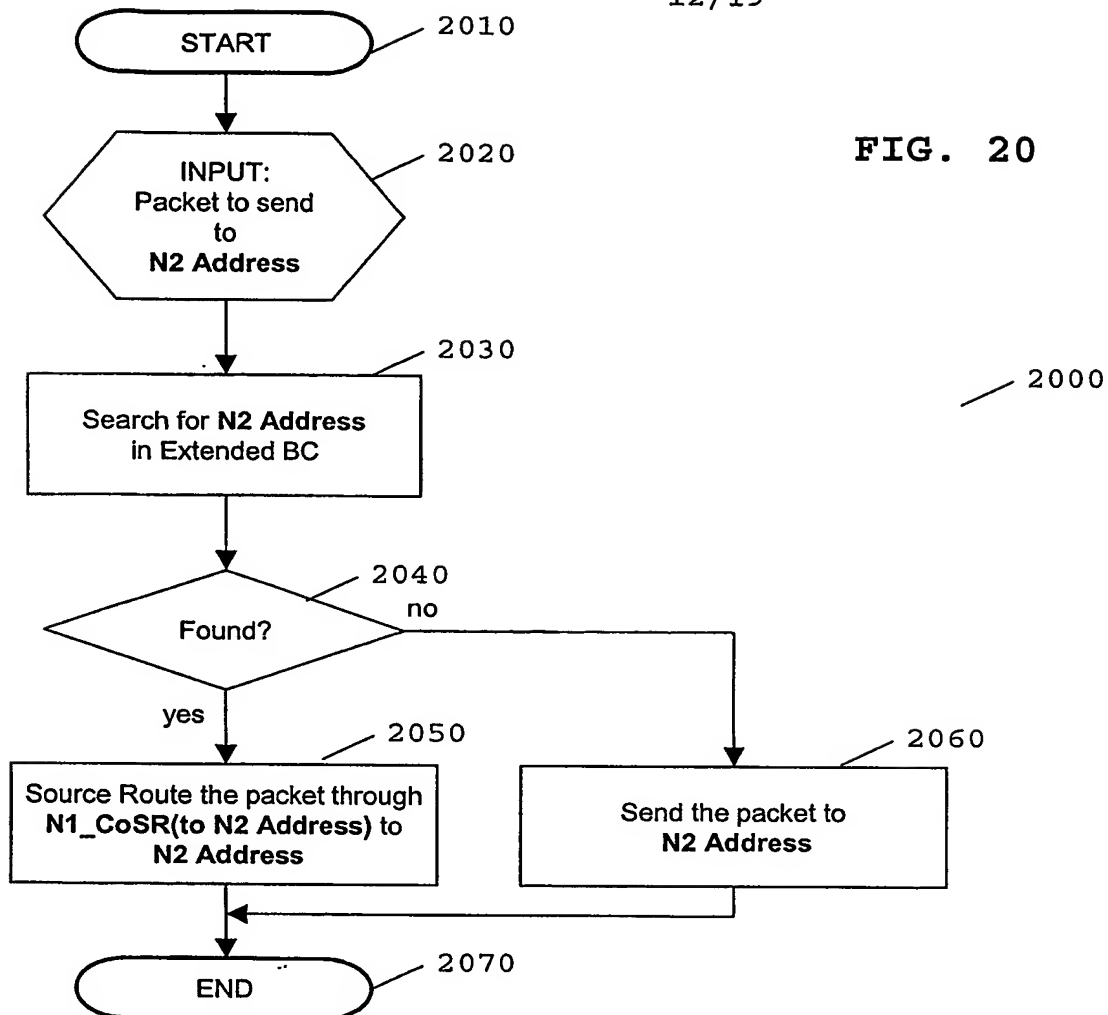
FIG. 16





12/19

FIG. 20



13/19

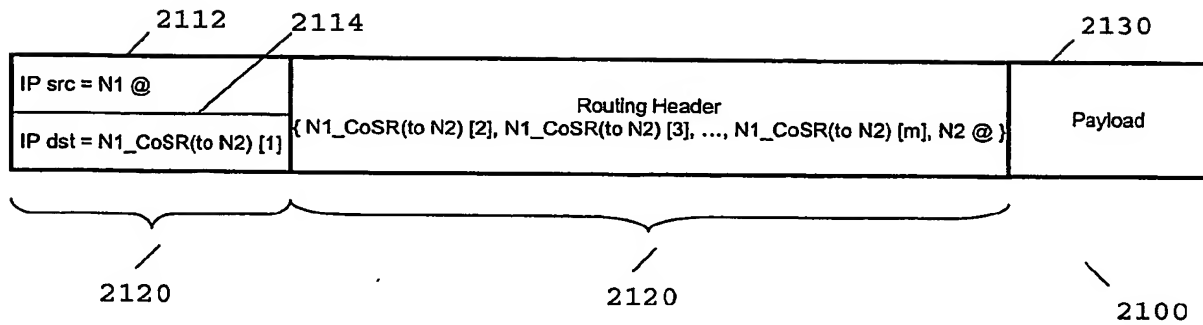
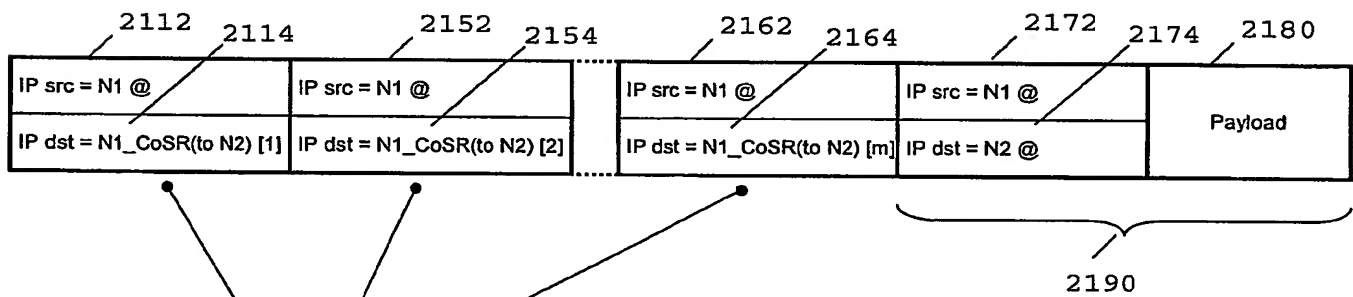


FIG. 21



*m consecutive IP headers with
addresses of CoSR_N1(to N2) as dst.*

FIG. 22

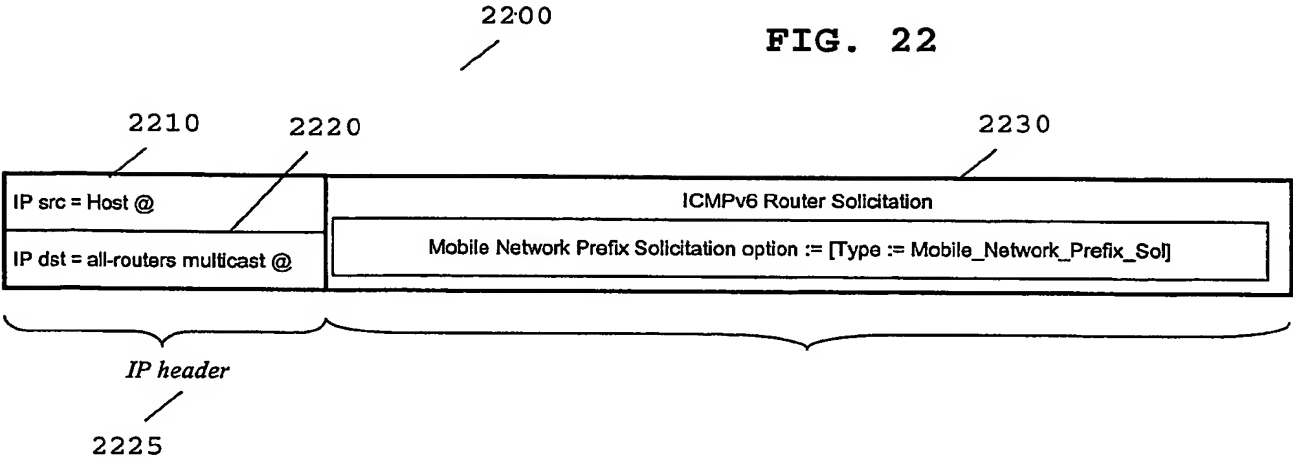


FIG. 23

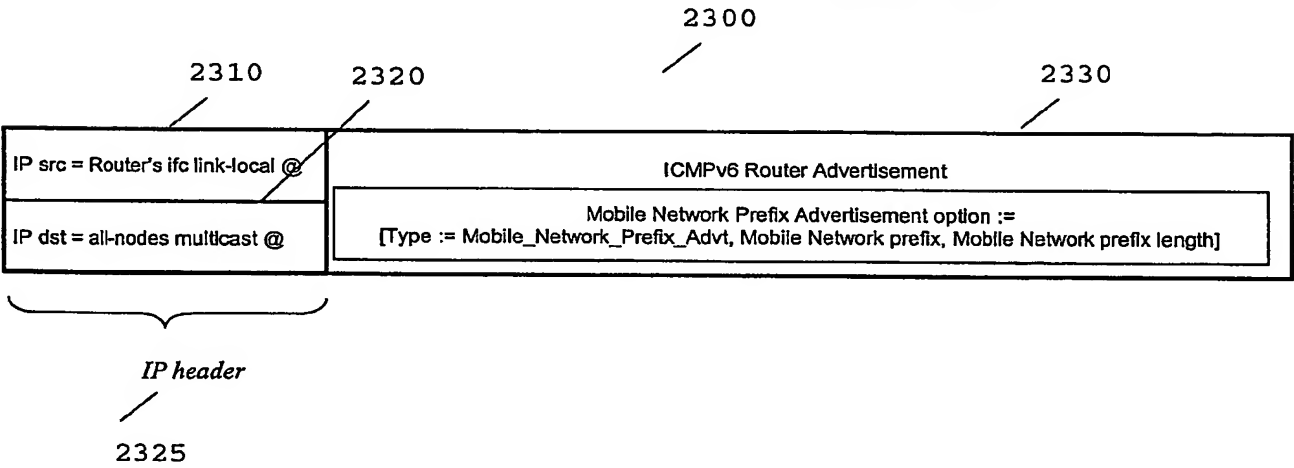


FIG. 24

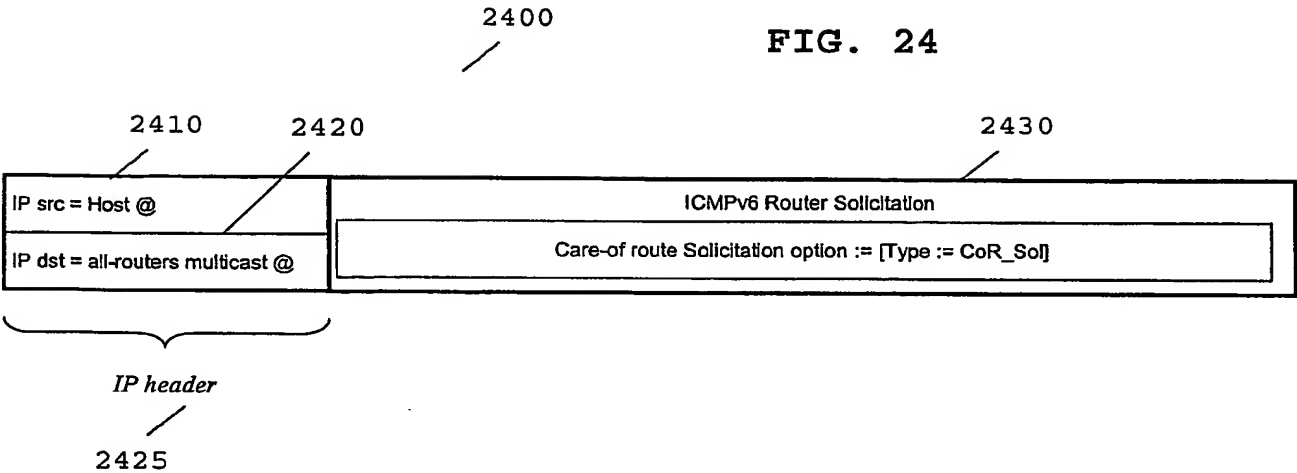


FIG. 25

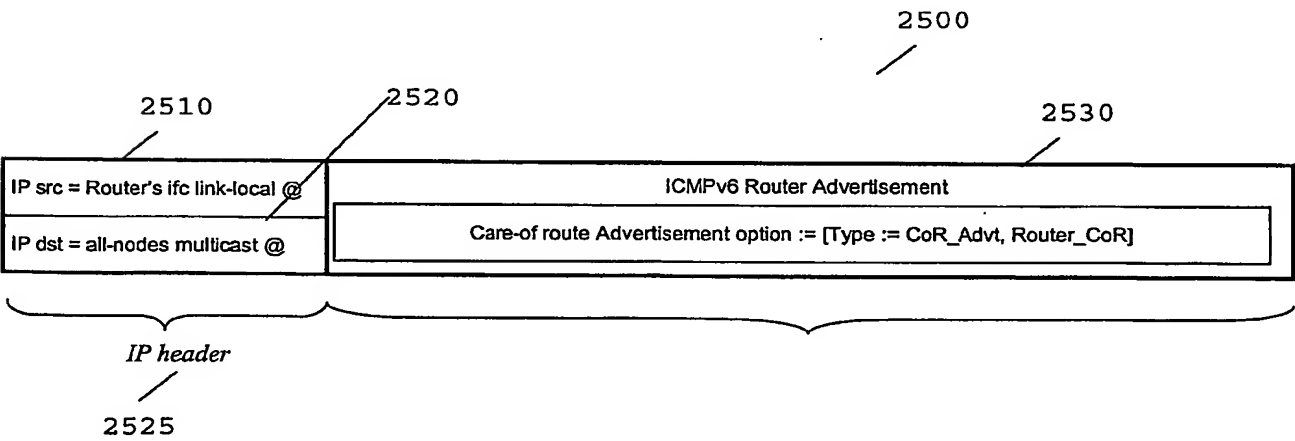


FIG. 26

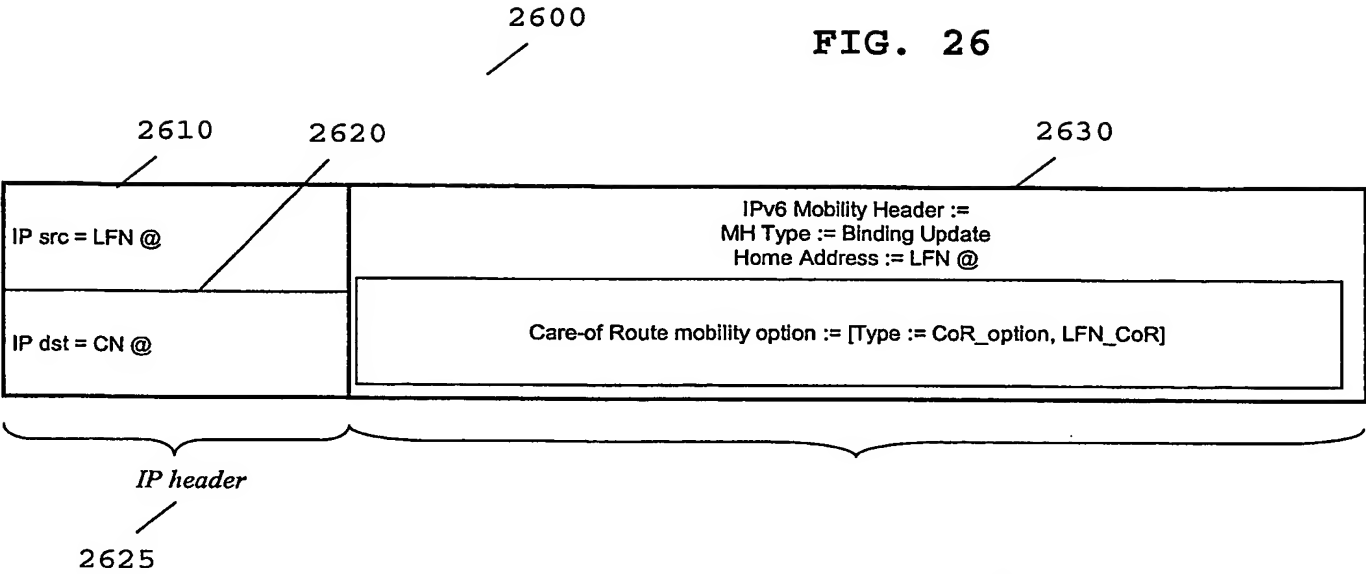
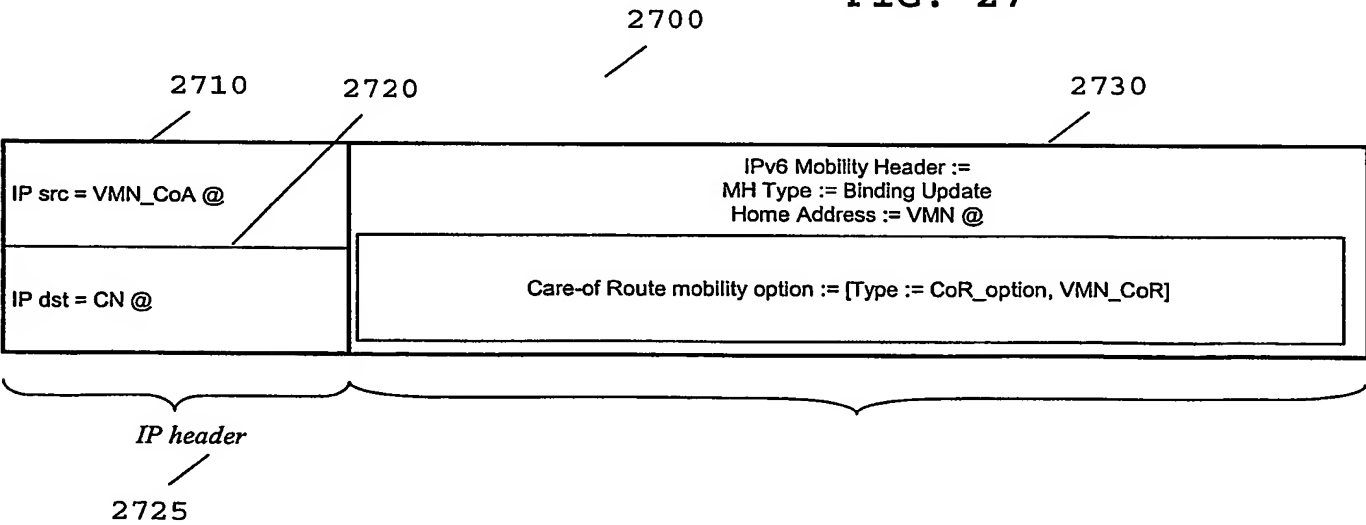


FIG. 27



17/19

FIG. 28

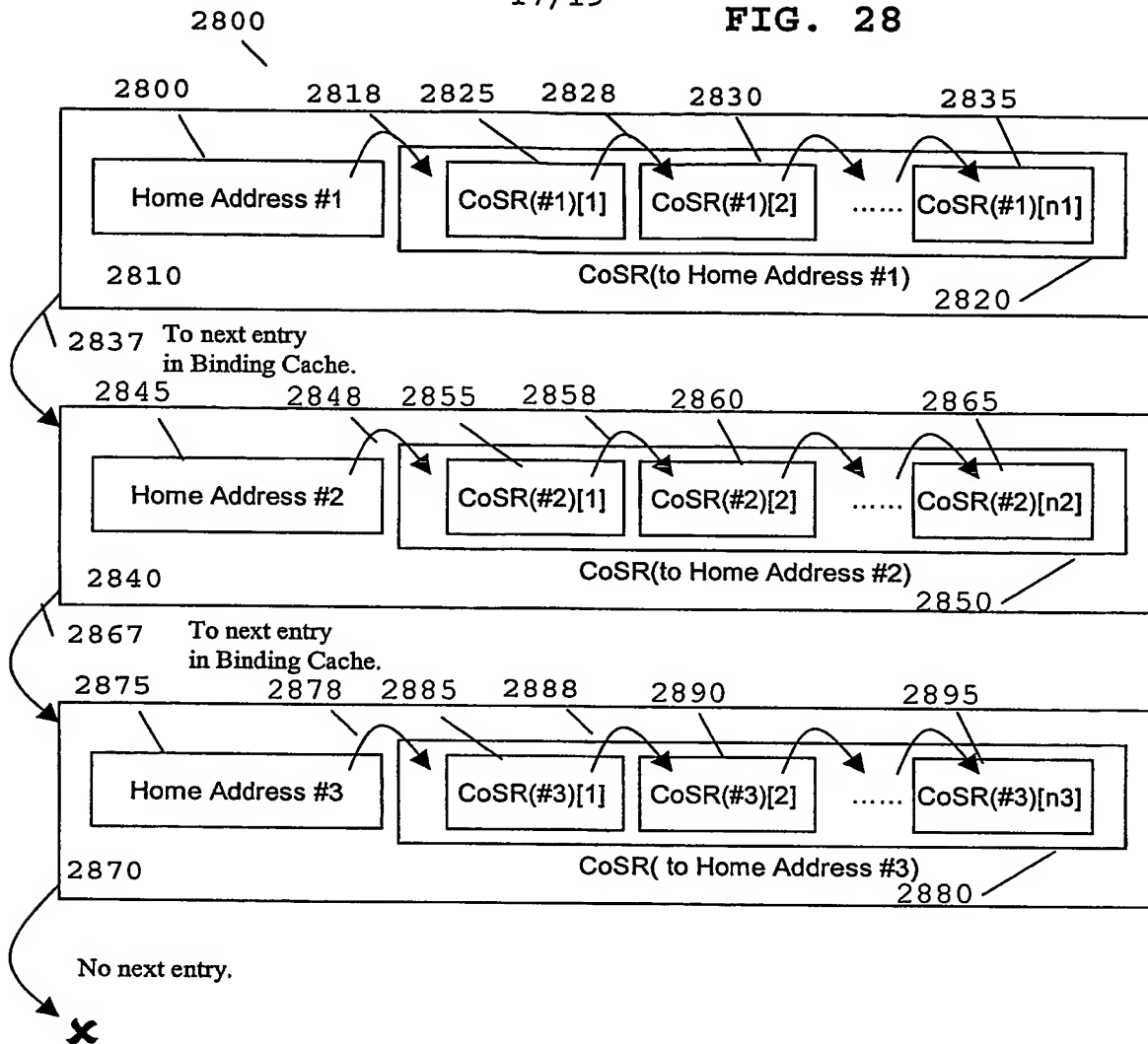
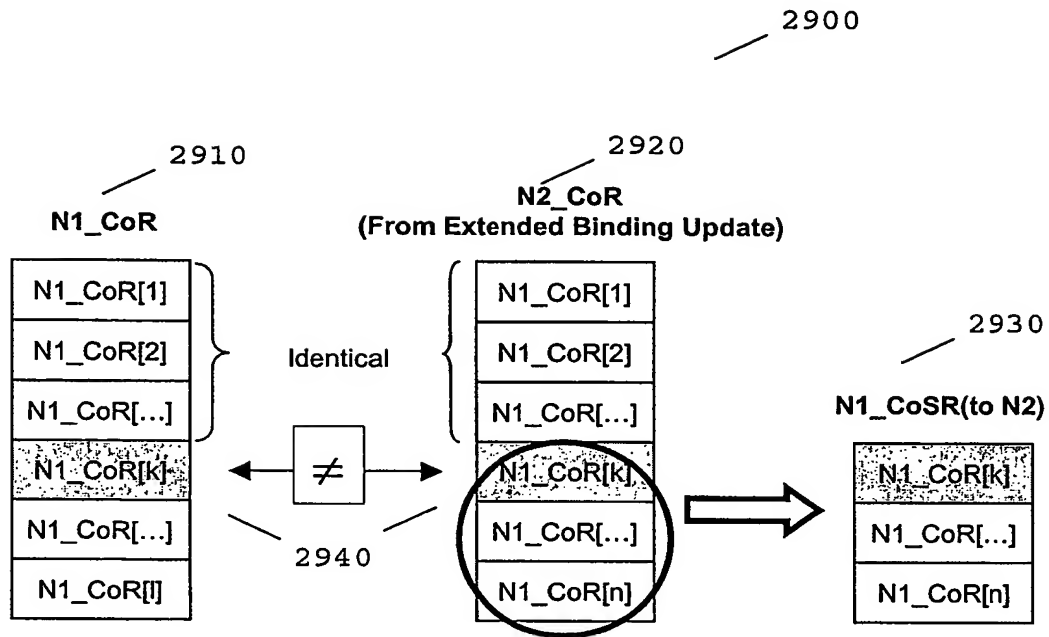


FIG. 29



19/19

FIG. 30

